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PATENT  
U.S. Application No. 10/573,793  
Attorney Docket No.: 13596/3

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT : Hidefumi TAKAYA, *et al.* *Confirmation No. 8004*  
SERIAL NO. : 10/573,793  
FILED : September 12, 2006  
FOR : INSULATED GATE TYPE SEMICONDUCTOR  
DEVICE AND MANUFACTURING METHOD THEREOF  
GROUP ART UNIT : 2826  
EXAMINER : Tuan N. QUACH

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO JUNE 20, 2008 OFFICE ACTION**

SIR:

In response to the non-final Office Action mailed June 20, 2008 in the above-identified application, the Applicant respectfully submits the following.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 9 of this paper.

**An Appendix** following page 12 of this paper.